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EICOSE

European Institute for Complex Safety
Critical Systems Engineering



Press Release

EICOSE accepted as first ARTEMIS Innovation Cluster

Cologne. EICOSE, the European Institute for Complex Safety Critical Systems Engineering, becomes the first ARTEMIS Innovation Cluster. Initially focused on transportation (automotive, aeronautics, space, railways), EICOSE may then in the future extend its scope towards critical applications in other domains (e.g. energy, healthcare). The ARTEMISIA Steering Board adopted the EICOSE proposal in a meeting in Cologne on Jan 31. Due to this step EICOSE accepts the responsibility for the field of processes, methods and tools for complex safety critical systems engineering of the ARTEMIS Strategic Research Agenda (SRA).

ARTEMIS, the European Technology Platform for Advanced Research & Technology for Embedded Intelligence and Systems, was established in 2004 and brings together key players from industry, small and medium-sized enterprises, universities, research centres and European public authorities in the field of Embedded Systems. Its overall aim is to reinforce European industry's leading position in Embedded System technology (according to the Lisbon Strategy, an action and development plan for the European Union aiming to "make Europe, by 2010, the most competitive and the most dynamic knowledge-based economy in the world").

One of the core objectives of ARTEMIS is to define a common Strategic Research Agenda (SRA) that will help coordinate research effort and initiatives across Europe in order to establish and implement a coherent and integrated European research and development strategy for Embedded Systems.

In its proposal for the 7th Framework Programme (FP7) the European Commission has introduced the concept of Joint Technology Initiatives (JTI) as a new way of developing joint public-private partnerships at European level. JTIs combine the resources of private industry, EU and national programmes to pursue ambitious common research objectives. ARTEMIS is a strong candidate to become a JTI. The recently established industrial association ARTEMISIA will rally European R&D actors to collectively represent them in the future JTI on embedded systems. The budget of the ARTEMIS JTI is expected to be around 3 billion EUR over seven years.

As an ARTEMIS Innovation Cluster, EICOSE will impact the research strategy of the expected ARTEMIS JTI, providing an innovation environment fostering R&D on embedded systems development for safety critical embedded applications, notably in the transportation domain. EICOSE currently maintains five working groups focused on Critical Embedded Systems

Development Environment (WG1), Affordable Safety (WG2), Design for Robustness (WG3), Human Assistance System Interaction (WG4), and Computing Platforms, gathering about 100 experts in critical systems development processes. EICOSE will feed back the findings of these domain oriented roadmapping activities into the ARTEMISIA roadmapping activities.

EICOSE builds on three national initiatives: French competitiveness clusters AEROSPACE VALLEY and SYSTEM@TIC PARIS-REGION and the German competence cluster SafeTRANS, which are led by major companies in their field and leading research institutes and universities. The approval of EICOSE as ARTEMIS Innovation Cluster allows ARTEMIS to leverage on the innovation environments of these three clusters. Other ARTEMISIA members are expected to become associated with EICOSE, thus contributing to the EICOSE Working Groups.

AEROSPACE VALLEY

The world-class competitiveness cluster AEROSPACE VALLEY aims at developing, on a national, European and international level, the competitiveness of the aerospace and embedded systems sector in Midi-Pyrénées and Aquitaine regions, southwest of France. In the field of embedded systems, AEROSPACE VALLEY addresses specially safety critical systems in aircrafts and spacecrafts, as well as in automotive and other terrestrial transport vehicles, promoting technology transfers between industries.

SafeTRANS

The German competence cluster SafeTRANS strives to become the leading competence network in processes and development methods for embedded safety critical applications and is dedicated to achieving the societal goal of maintaining high safety levels of transportation systems through coordinated research in the area of safety critical embedded systems engineering.

SafeTRANS drives research in human centred design, in systems and software development methods for embedded systems, as well as in safety analysis and – for avionics and rail – its integration in certification processes.

SYSTEM@TIC PARIS-REGION

The world-class competitiveness cluster SYSTEM@TIC PARIS-REGION aims to maintain the leadership of large scale integrators by supporting their R&D activities in the Paris Region, contribute to the creation and growth of technological SME with global ambitions, and promote the Paris Region's image of excellence abroad. The cluster focuses on software methods and tools enabling the development of embedded systems related to four booming target markets: Telecoms, Security & Defence, Automotive & Transport, System Design and Development Tools.

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